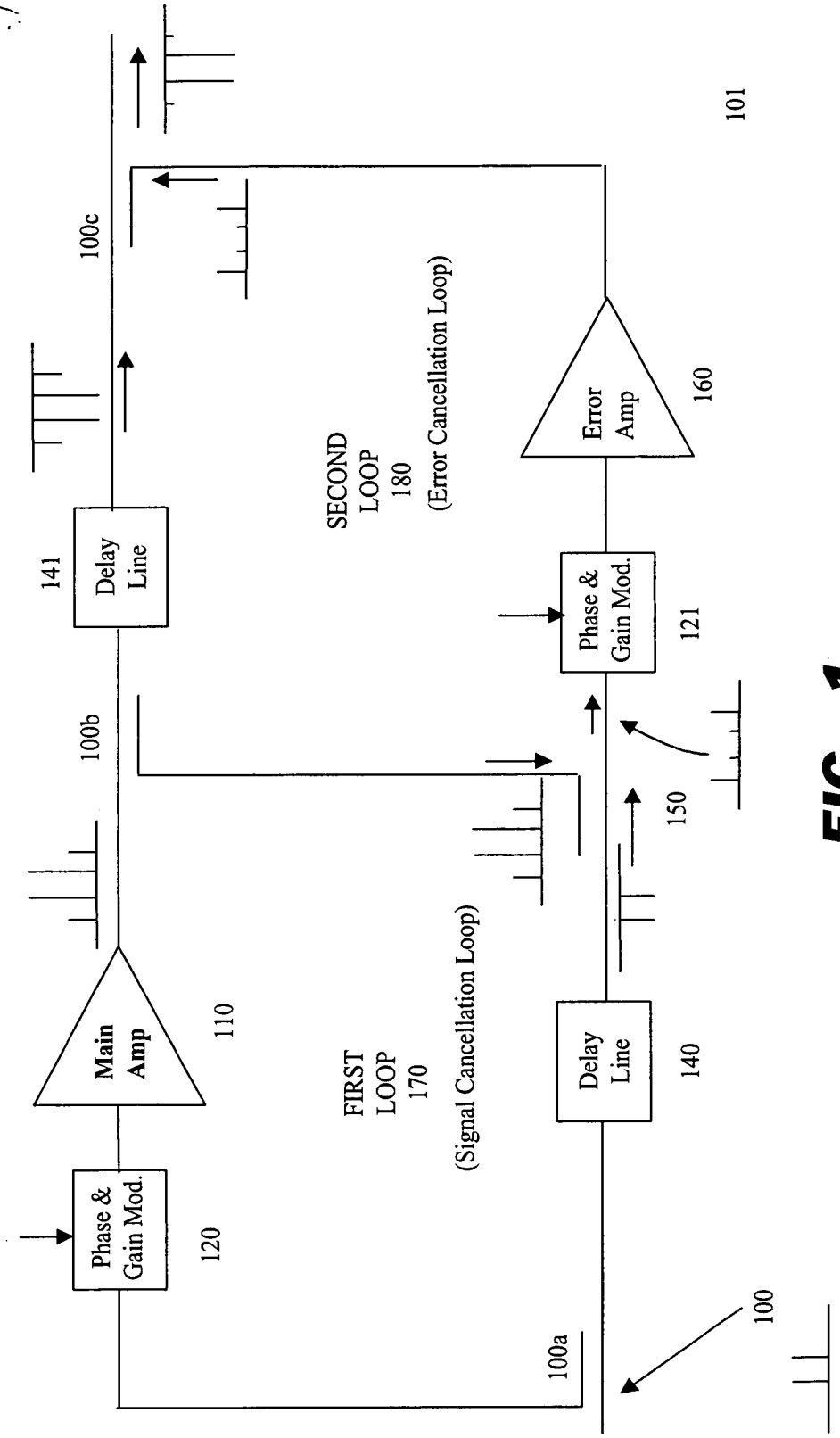


FIG. 1
PRIOR ART

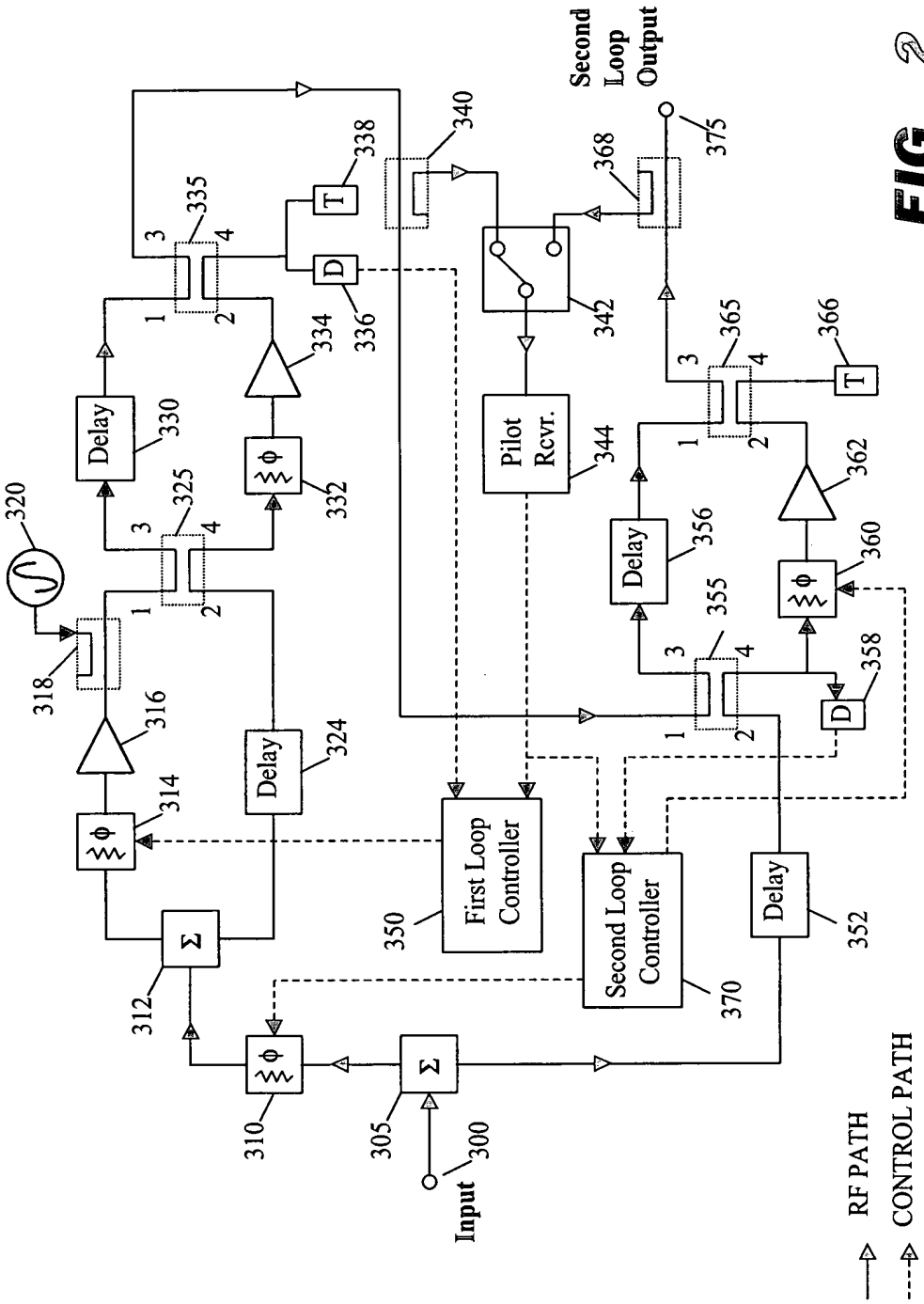


FIG. 2
PRIOR ART

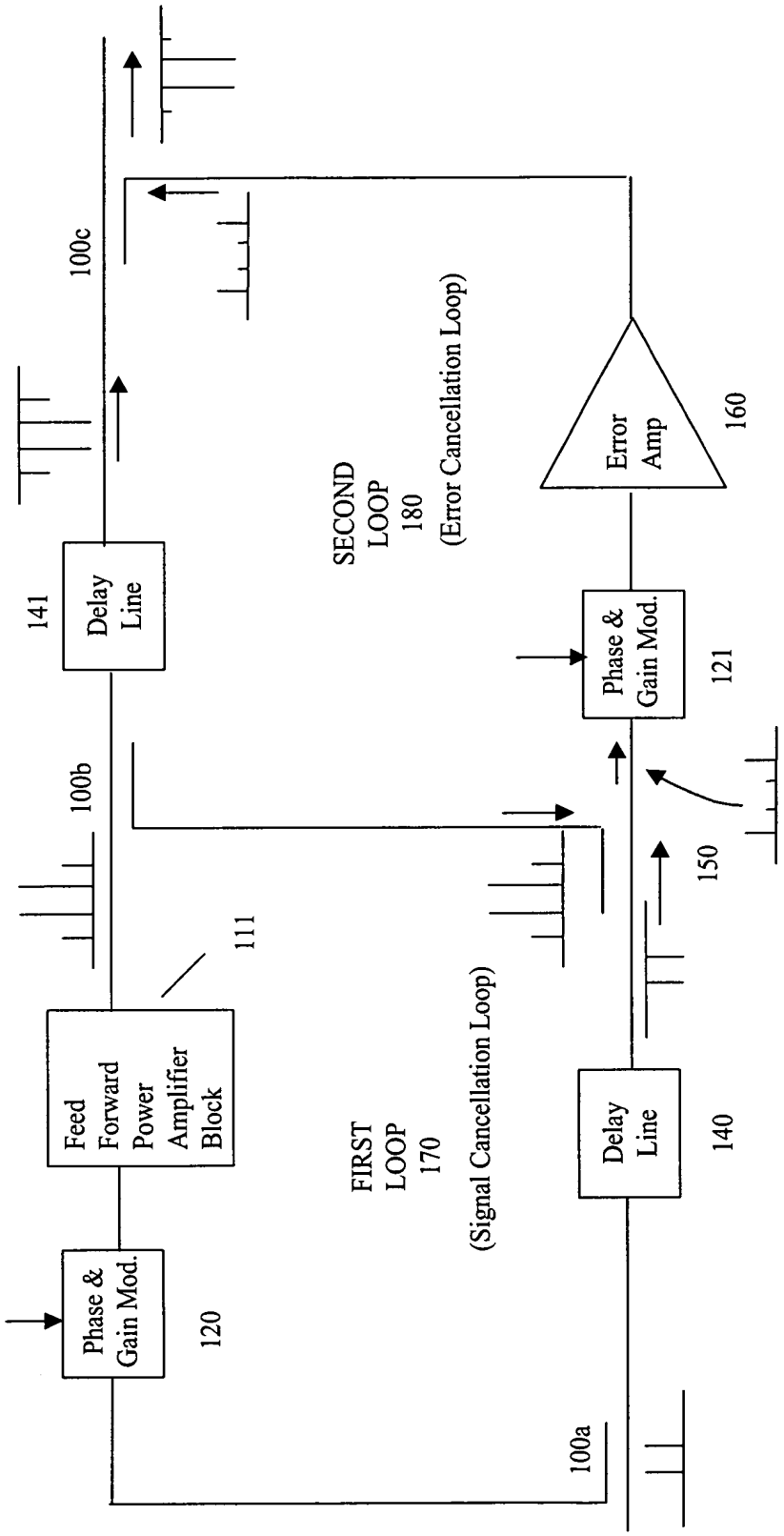
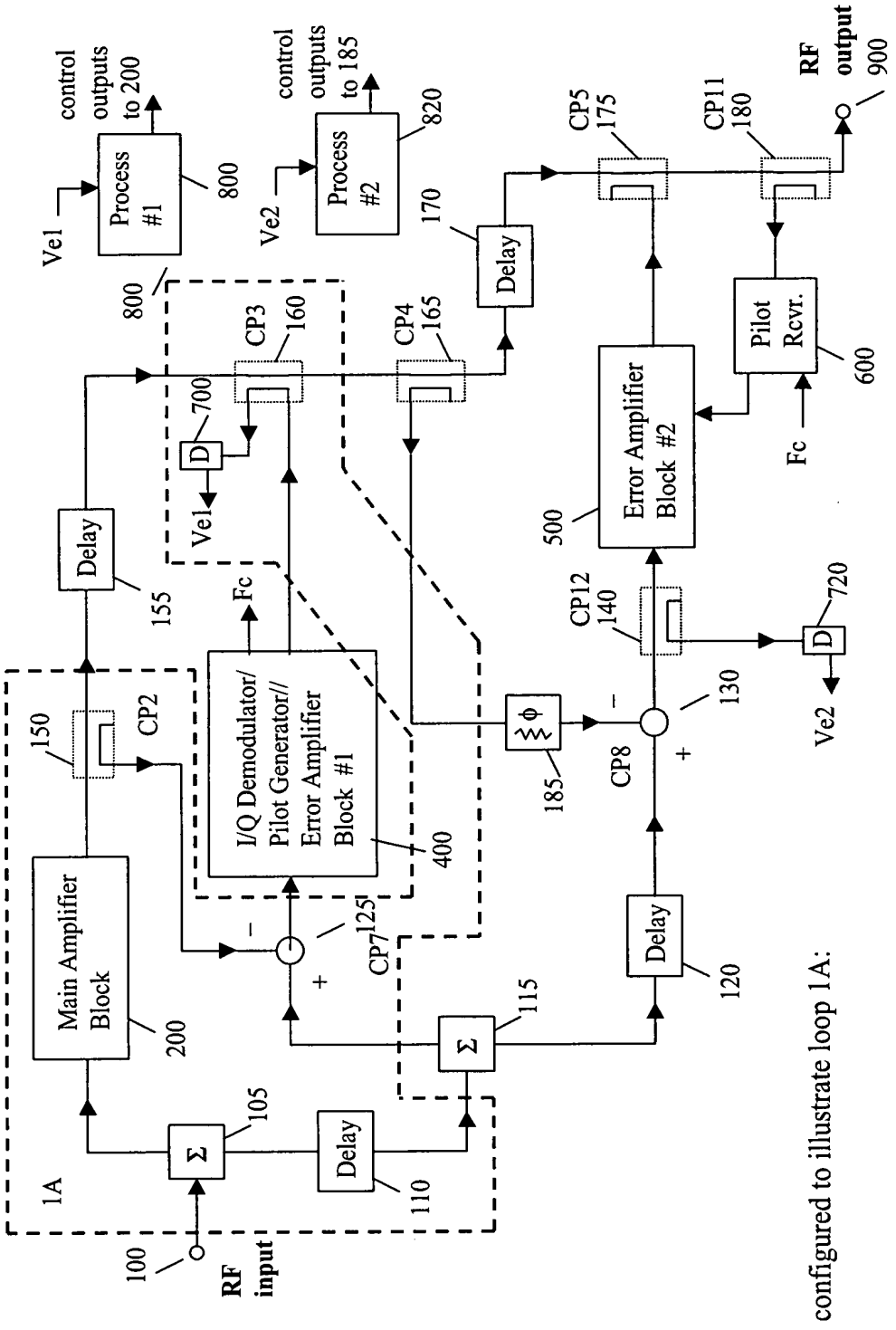


FIG. 3
PRIOR ART



configured to illustrate loop 1A:

FIG. 4

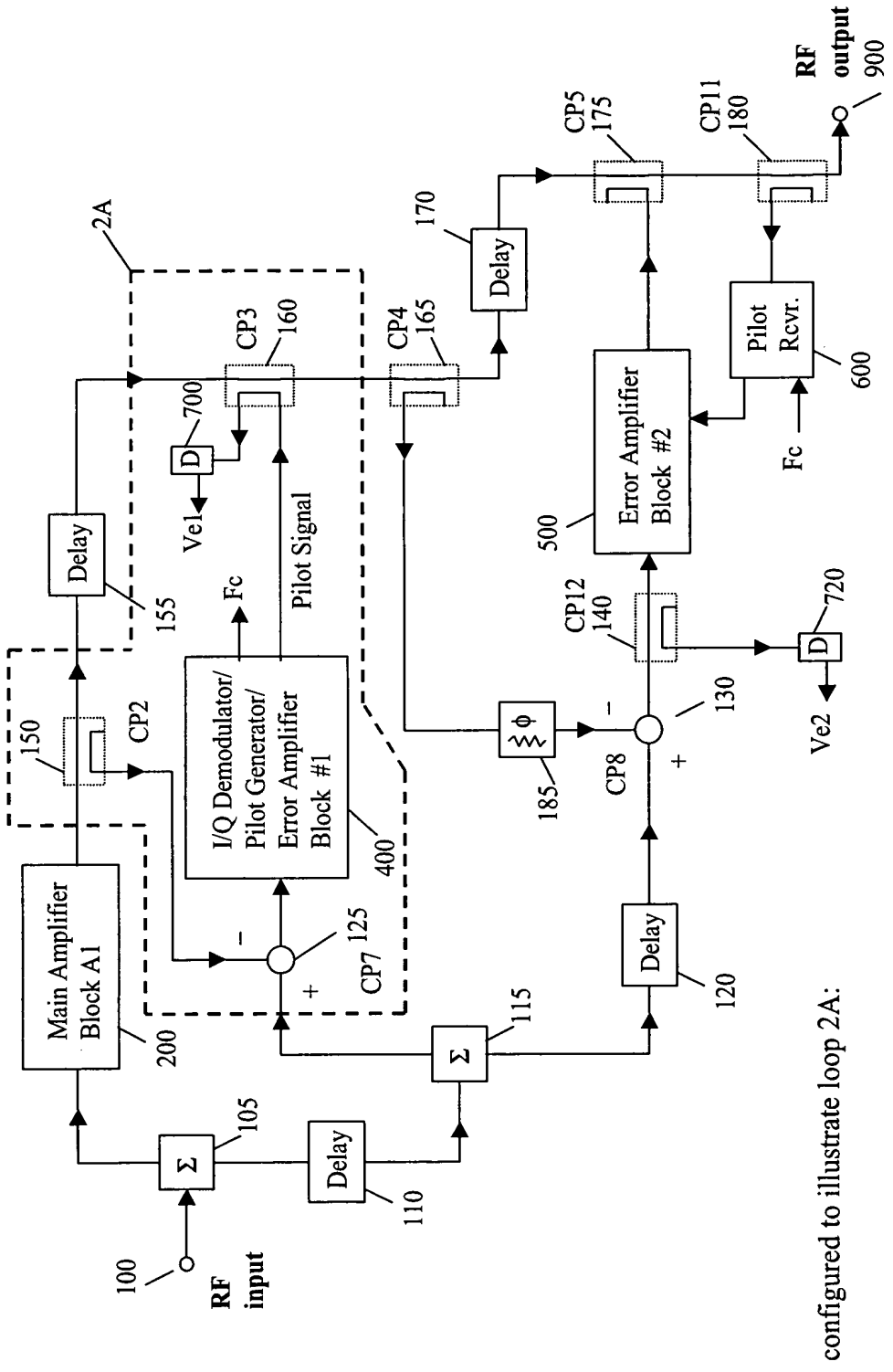


FIG. 5

configured to illustrate loop 2A:

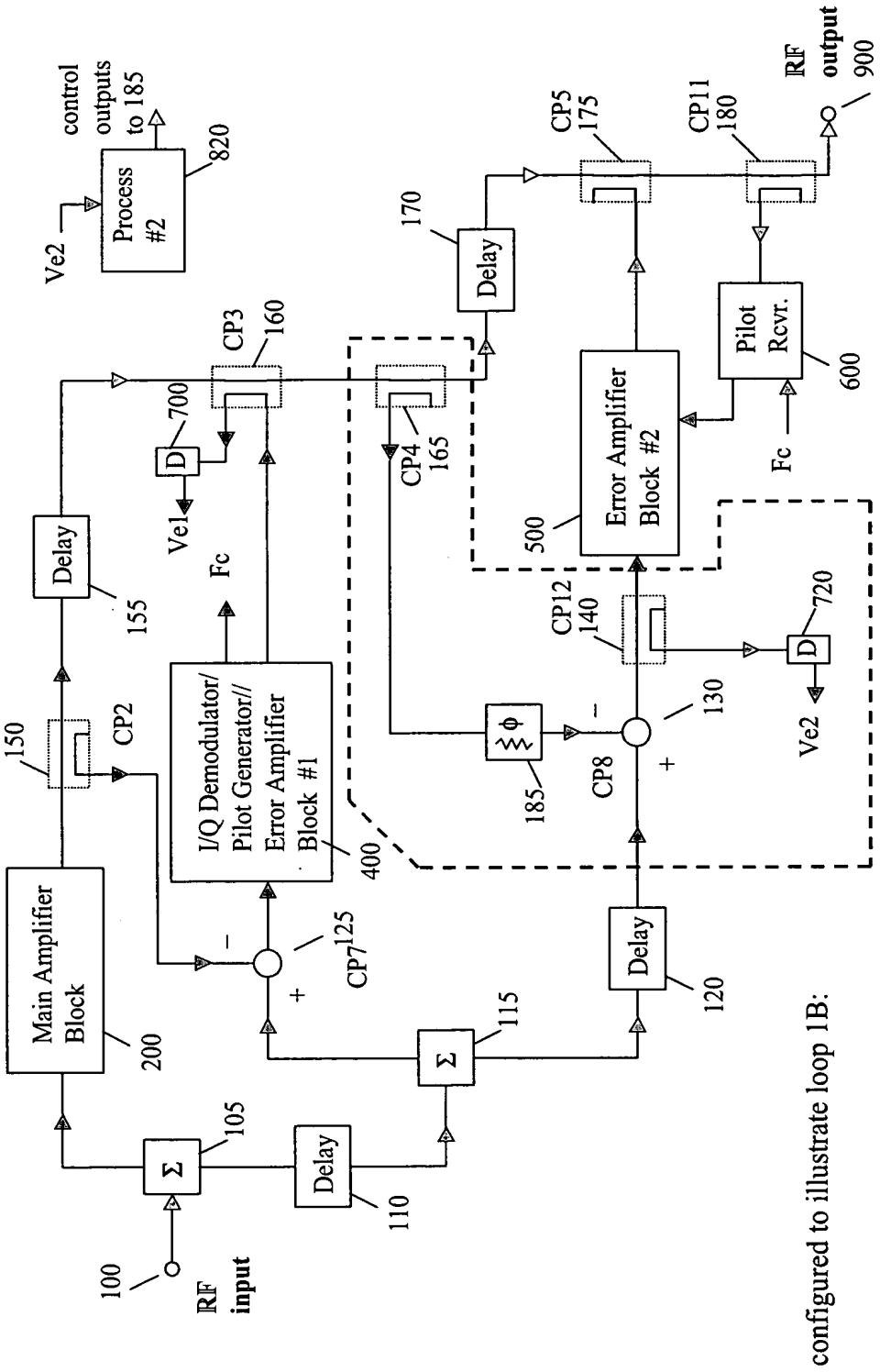
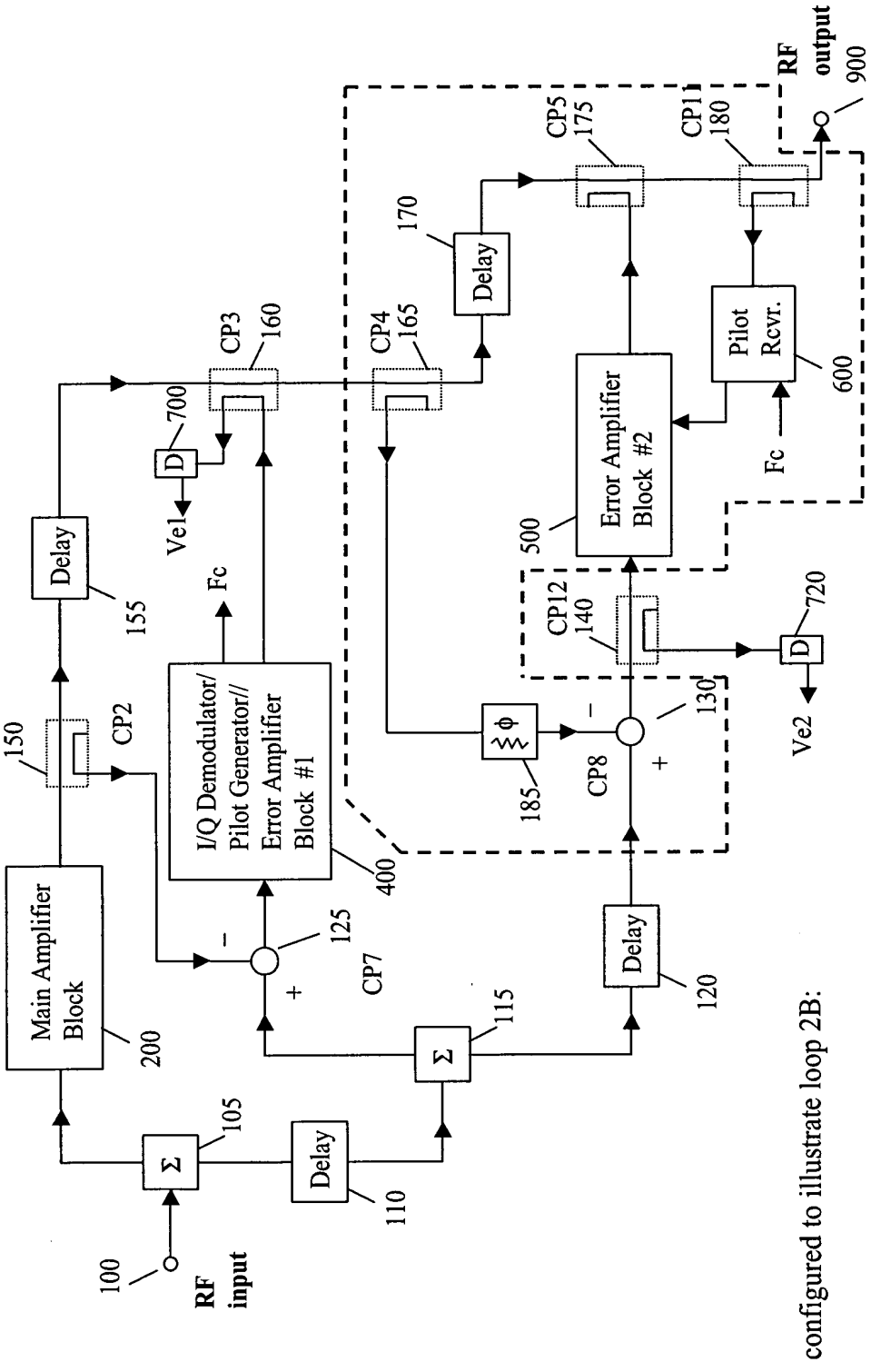


FIG. 6

configured to illustrate loop 1B:



configured to illustrate loop 2B:

FIG. 7

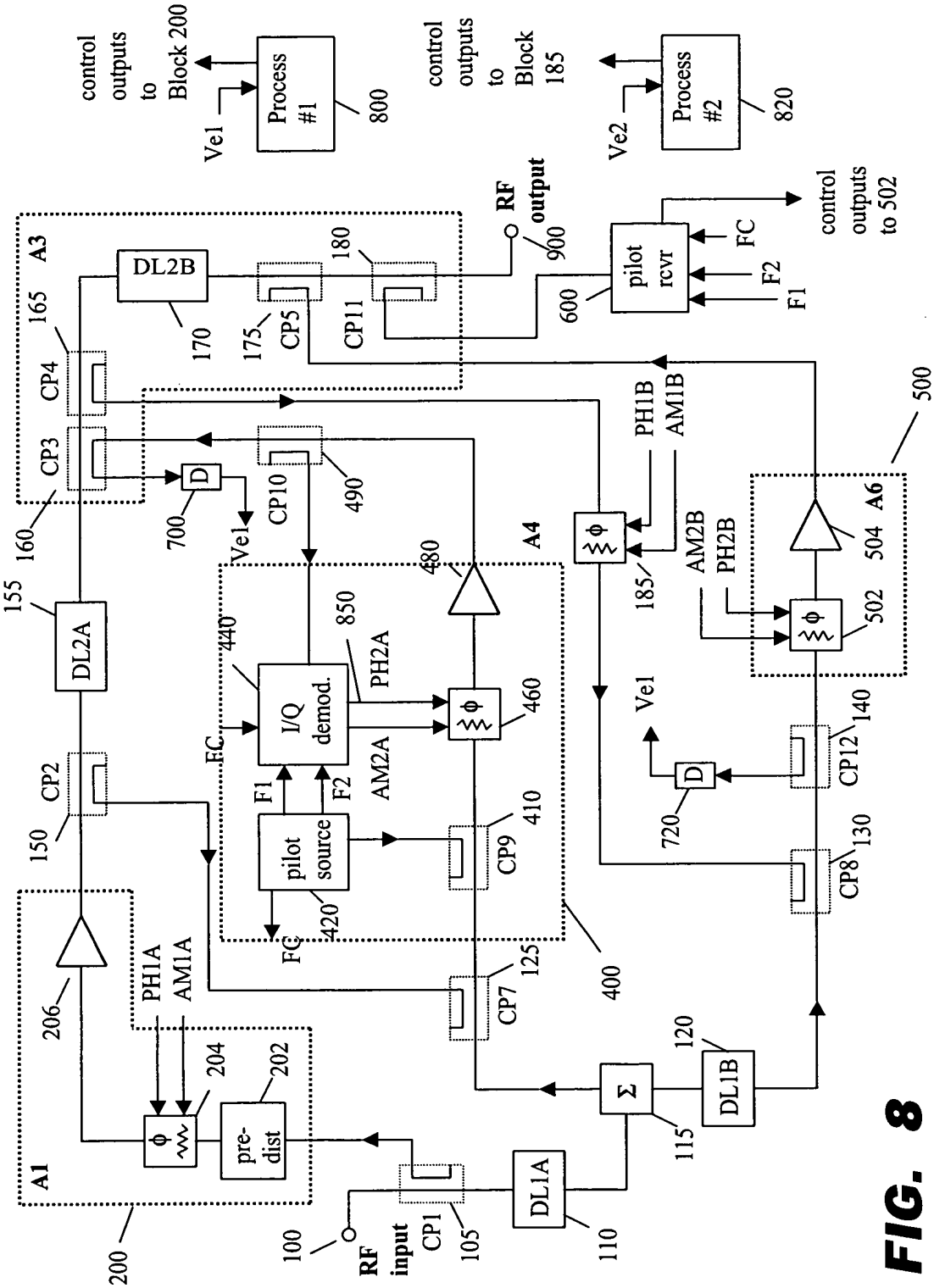


FIG. 8

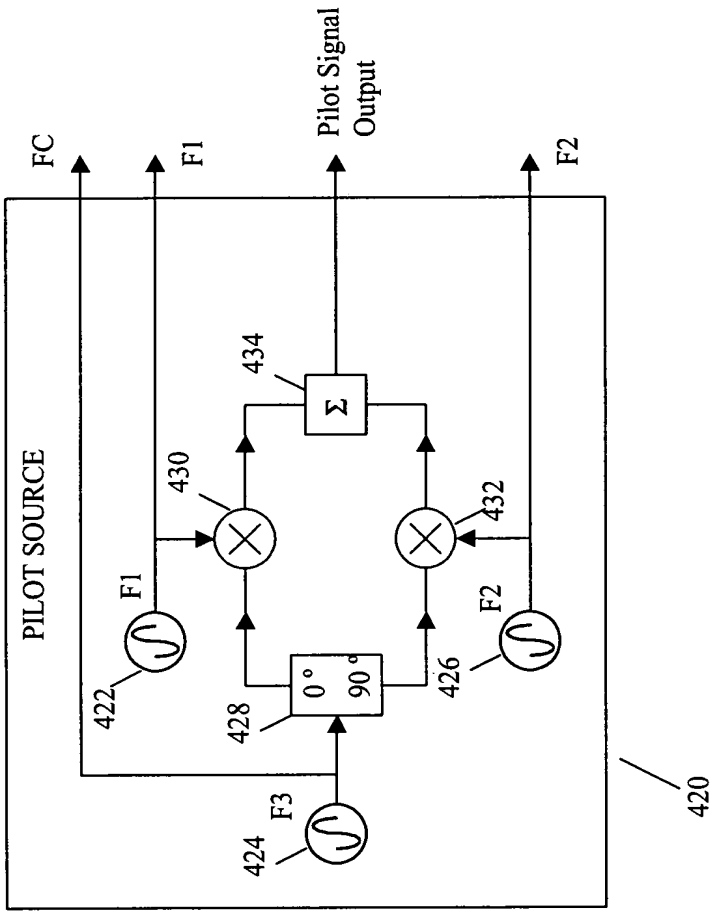


FIG. 9

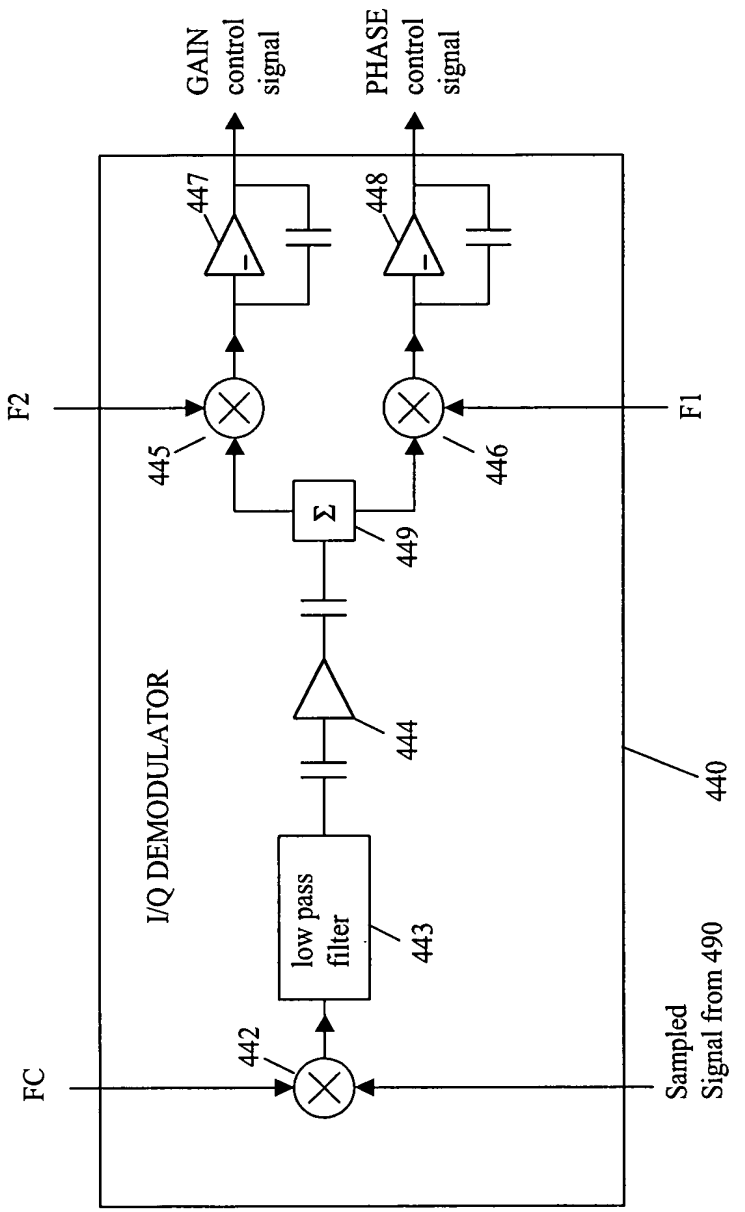


FIG. 10

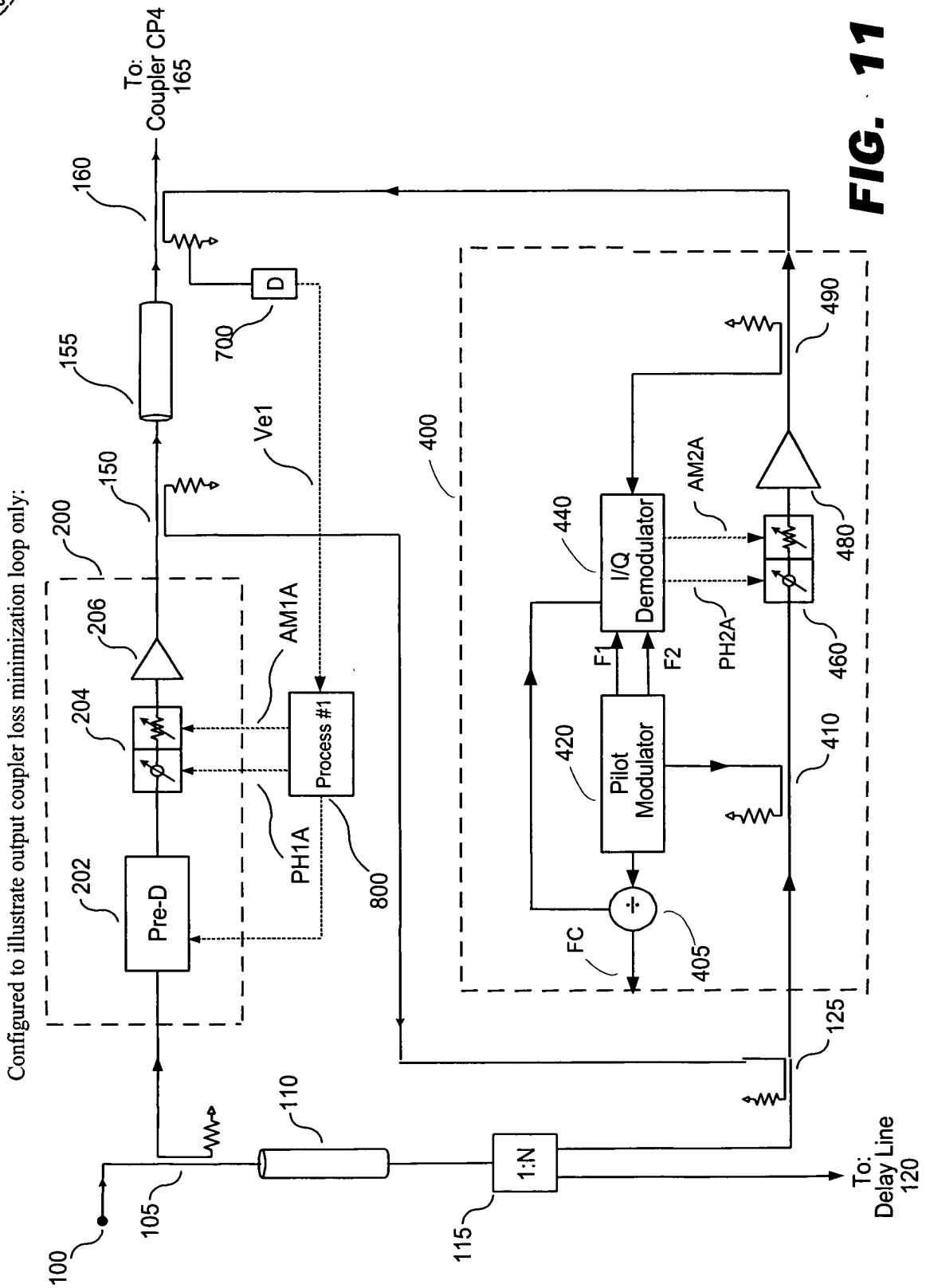


FIG. 11

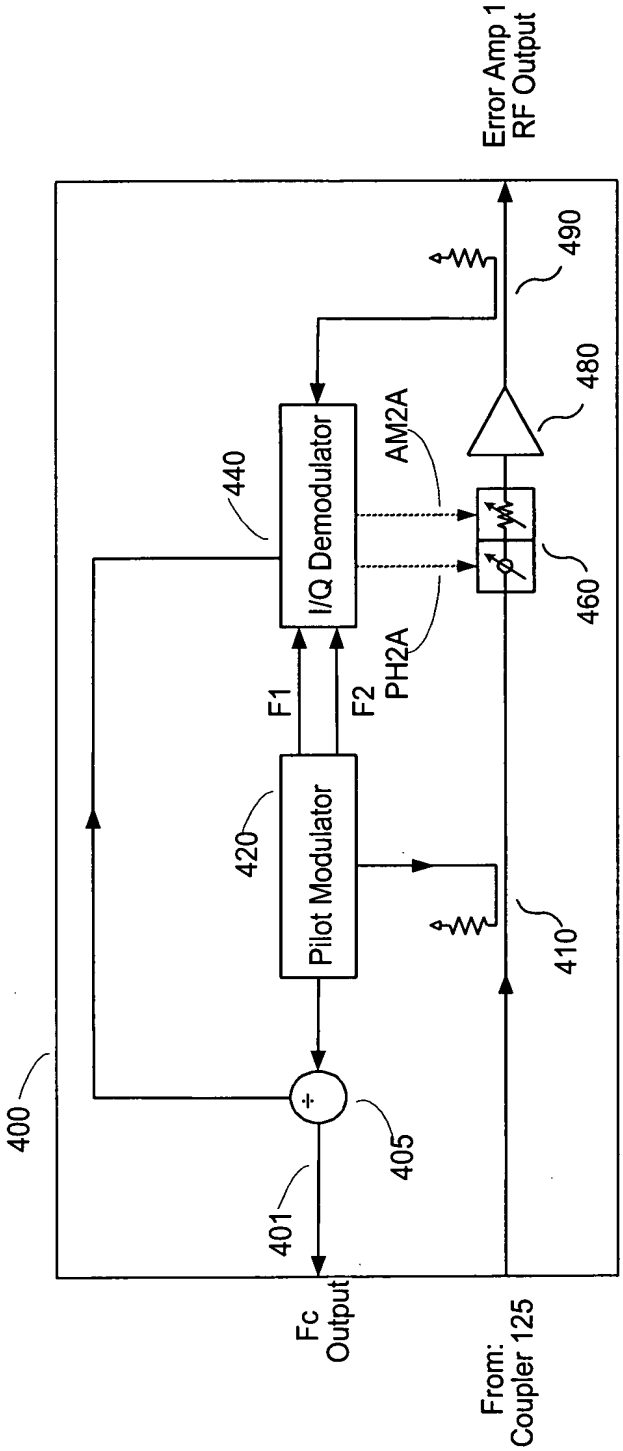


FIG. 12

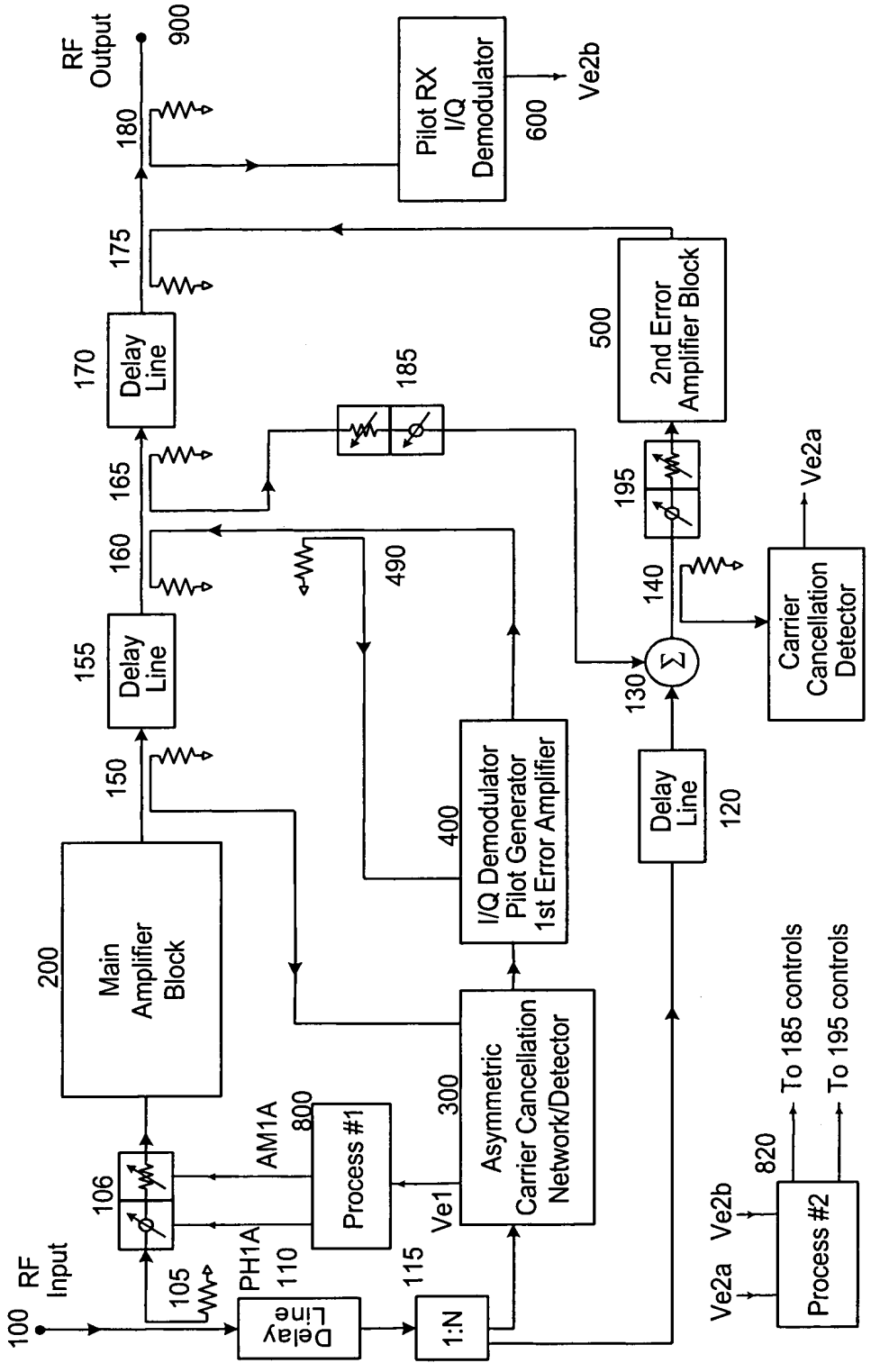


FIG. 13A

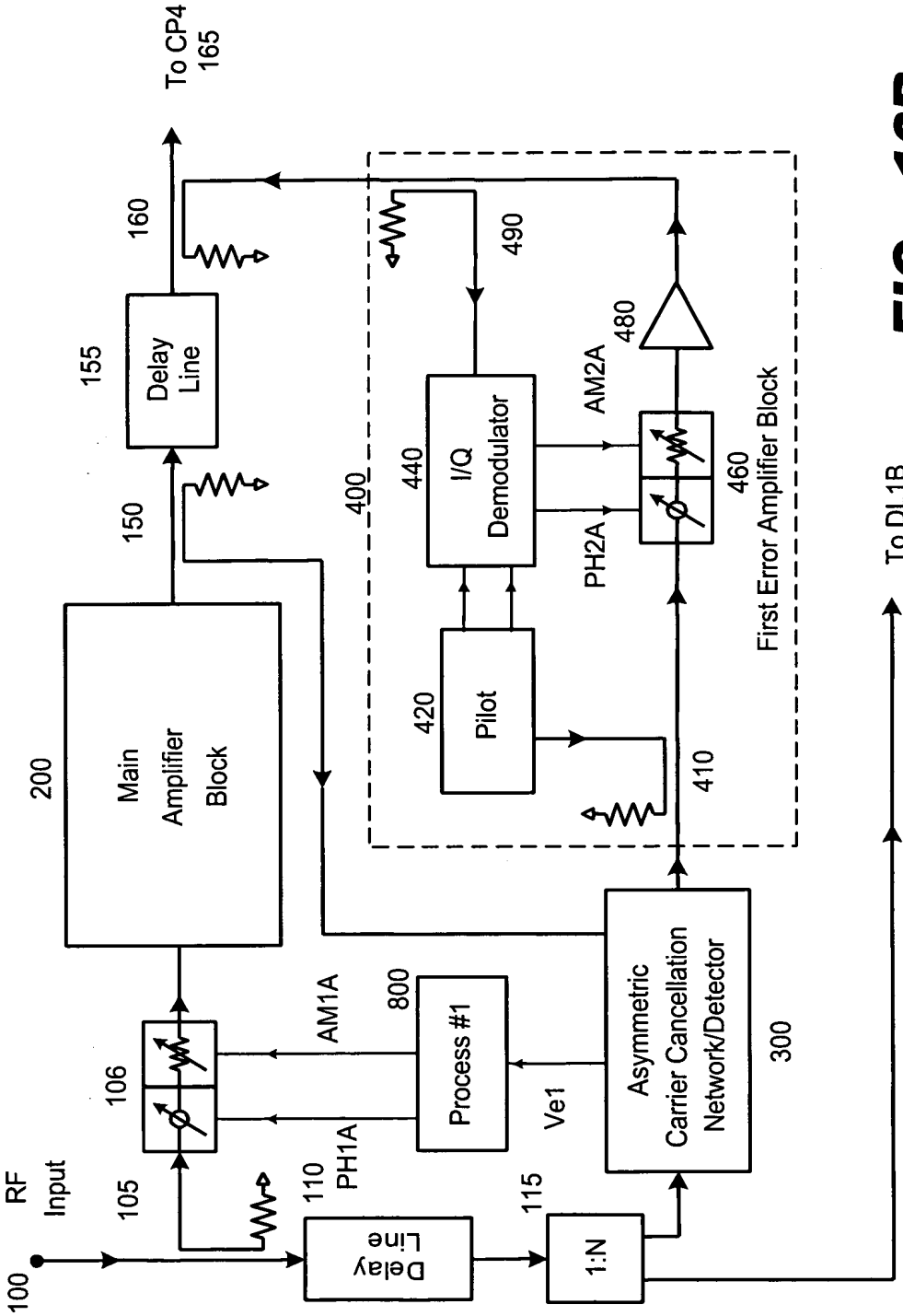


FIG. 13B

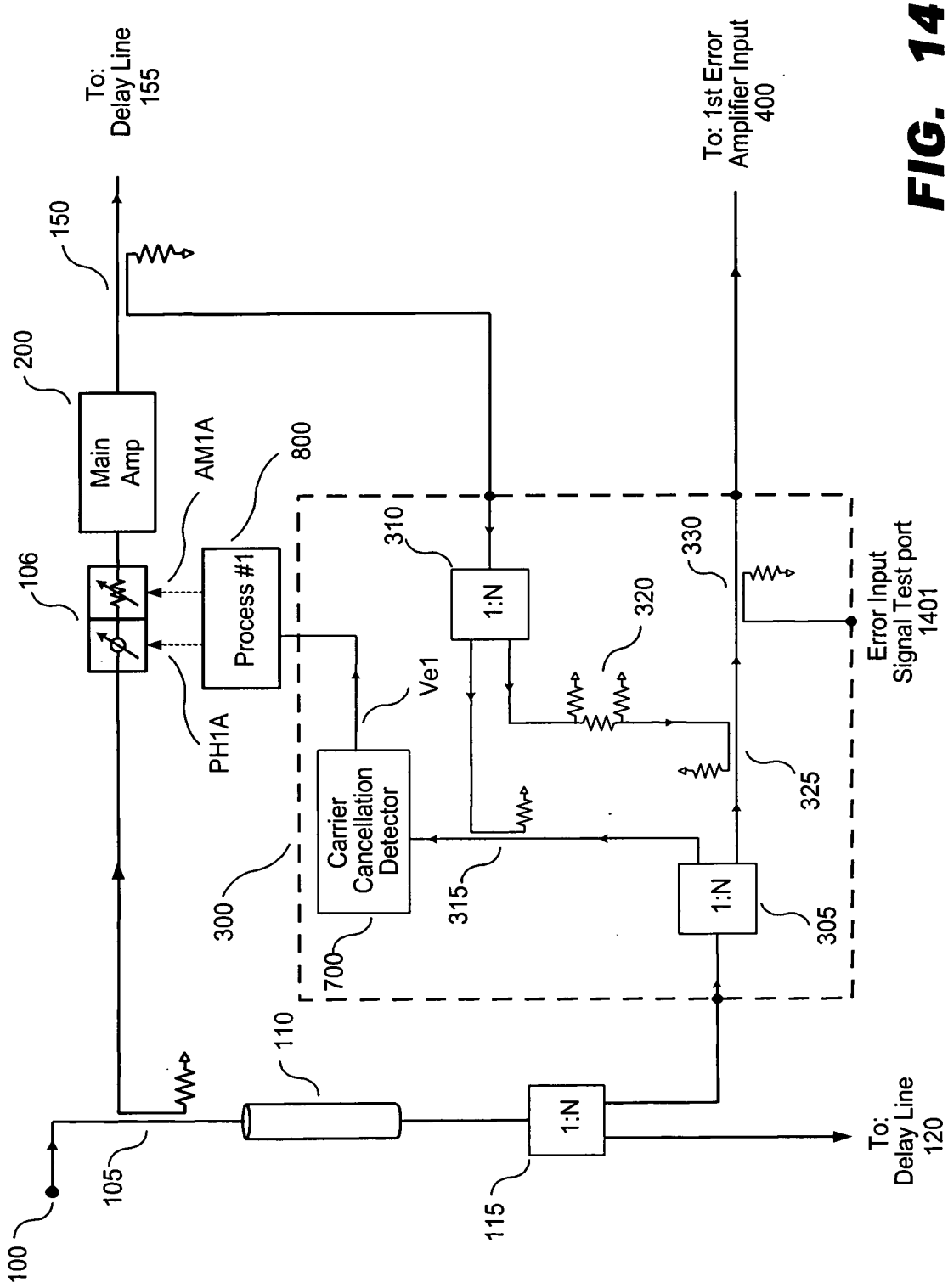


FIG. 14

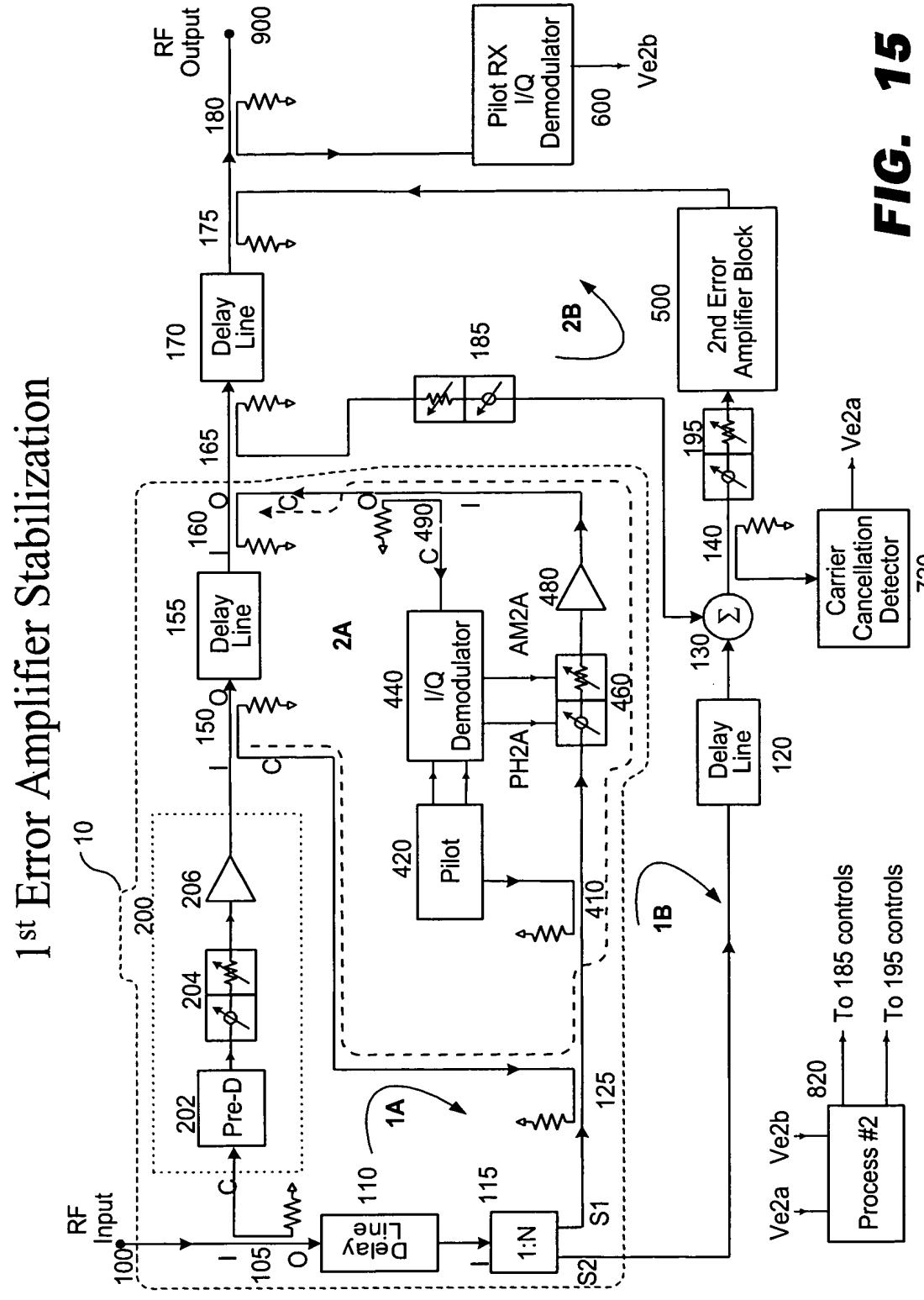


FIG. 15